

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: REARDON ET AL.  
APPLICATION NO.: 09/893,316  
FILED: June 26, 2001  
FOR: SEMICONDUCTOR PROCESSING  
APPARATUS

EXAMINER: G.R. KOCH  
ART UNIT: 1734  
CONFIRMATION NO: 2811

**Comments on Statement of Reasons for Allowance**

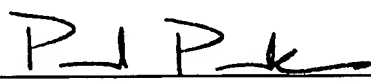
Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In the Notice of Allowability mailed June 9, 2006, the Examiner allowed claims 54-102 and 104-108. Although the applicant's attorney agrees with the Examiner's conclusion that these claims are allowable, the applicant's attorney notes that the claims may be allowable for reasons other than those identified by the Examiner and does not concede that the Examiner's characterizations of the terms of the claims and the prior art are correct.

Respectfully submitted,  
Perkins Coie LLP

Date: 12 June 2006

  
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